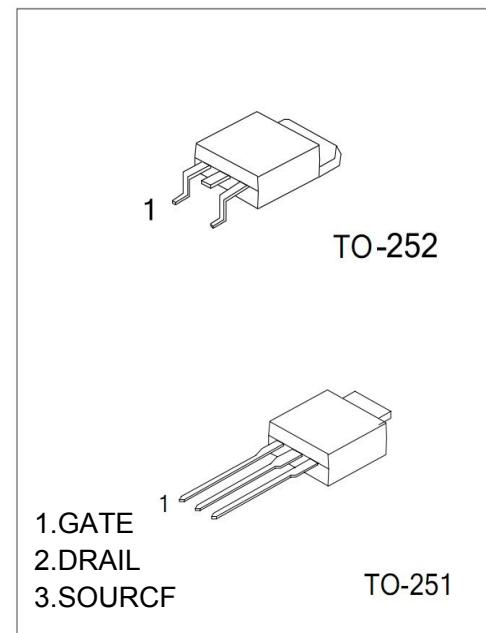


TO-252 / 251 Plastic-Encapsulate MOSFETS
BY3004N
N-Channel 30-V(D-S) Power MOSFET

V(BR)DSS	RDS(on)MAX	ID
30 V	5mΩ@ 10 V	80A
	6mΩ@ 4.5 V	

Equivalent Circuit:

General Description:

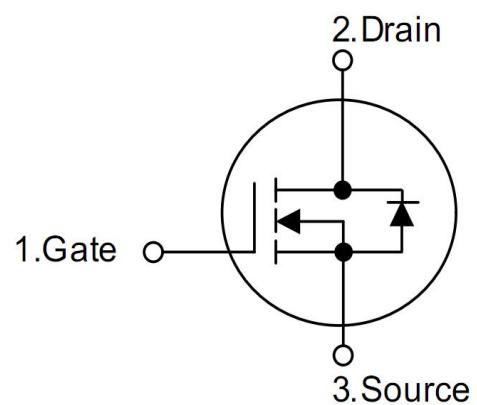
The high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition , this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes . The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power suppliers, converters and PWM motor controls , these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

MARKING: 3004N

 (D-252) / (U-251)

FEATURE:

- ※ Power switching application
- ※ Hard switched and high frequency circuits
- ※ Uninterruptible power supply
- ※ Fully characterized avalanche voltage and current
- ※ Excellent package for good heat dissipation
- ※ Good stability and uniformity with high EAS

SYMBOL:

Maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	VGS	±20	
Continuous Drain Current	ID	80	A
Pulsed Diode Current	IDM	180	
Power Dissipation	PD	53	W
Thermal Resistance from Junction to Ambient (t≤10s)	R _{θJA}	100	°C/W
Operating Junction	T _J	150	°C
Storage Temperature	T _{STG}	-55~+150	

MOSFET ELECTRICAL CHARACTERISTICS

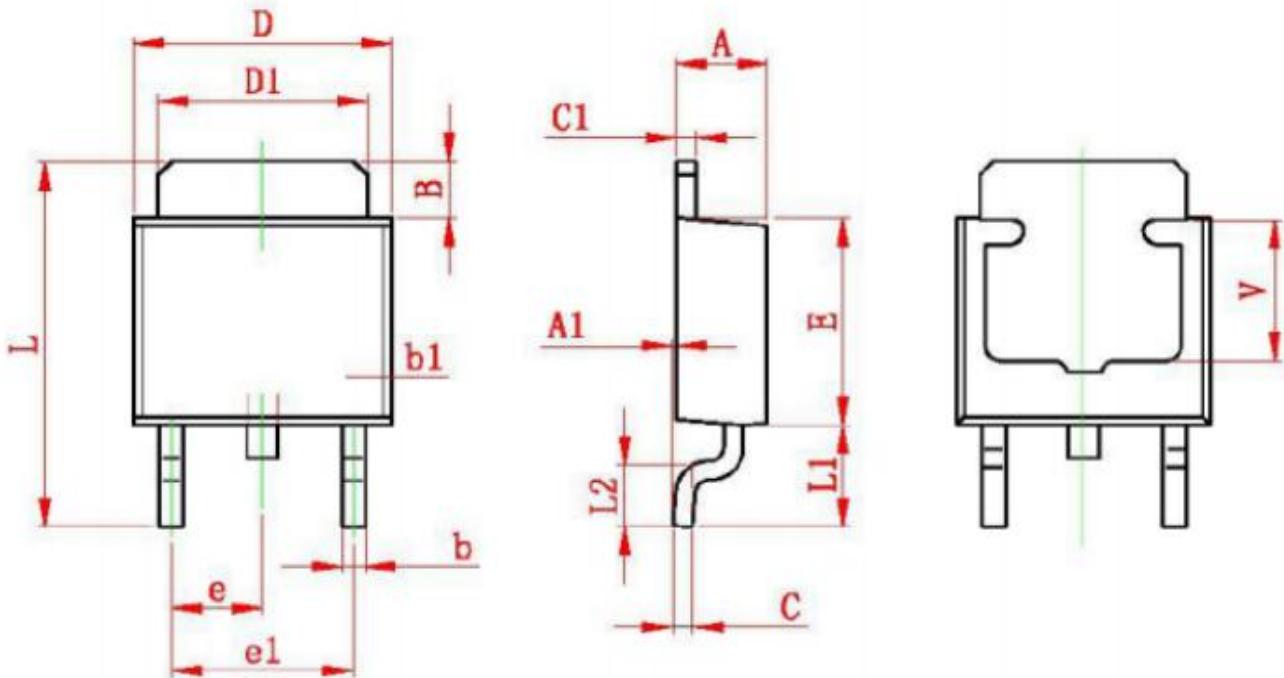
Static Electrical Characteristics (Ta = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-source breakdown voltage	V(BR)DSS	VGS = 0V, ID = 250µA	30			V
Gate-source threshold voltage	VGS(th)	VDS = VGS, ID = 250µA	1		3	V
Gate-source leakage	IGSS	VDS = 0V, VGS = ±20V			±100	nA
Zero gate voltage drain current	IDSS	VDS = 25V, VGS = 0V			1	µA
Drain-source on-state resistancea	RDS(on)	VGS = 10V, ID = 15A		4	5	mΩ
	RDS(on)	VGS = 4.5V, ID = 18A		5.2	6	mΩ
Forward transconductancea	gfs	VDS = 25V, ID = 30A		35		S
Diode forward voltage	VSD	IS= 3A, VGS=0V		0.8	1.3	V
Dynamic						
Input capacitance	Ciss	VDS = 25V, VGS = 0V, f=1MHz		1485		pF
Output capacitance	Coss			245		pF
Reverse transfer capacitanceb	Crss			170		pF
Total gate charge	Qg	VDS = 25V, VGS = 10V, ID = 33A		16.5		nC
Gate-source charge	Qgs			5		nC
Gate-drain charge	Qgd			10.3		nC
Gate resistance	Rg	f=1MHz		2		Ω
Switchingb						
Turn-on delay time	td(on)	VDD= 25V RL= 18Ω, ID = 33A, VGEN= 10V,Rg= 18Ω		8.2		ns
Rise time	tr			105		ns
Turn-off delay time	td(off)			22		ns
Fall time	tf			8.5		ns
Drain-Source Diode Characteristics						
Continuous Source-Drain Diode Current	IS				80	A
Pulsed Diode forward Current	ISM				195	A

Note :

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t < 10 sec.
3. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production testing.

PACKAGE OUTLINE DIMENSIONS :



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP		0.091 TYP	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
V	3.80 REF		0.150 REF	

PACKAGE OUTLINE DIMENSIONS :

